

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS
IN AN RF POWER AMPLIFIER

Application Number : 10/743221



Confirmation Number: 7643

First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

Art Unit: 2817

Examiner: Steven J. Mottola

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or 5945878 or 6259325 or 6448847 or 6809581 or 20050134378 or 20050134386 or
20050134375).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	3684975	1972-08-01	Valfre, Cesare			
	2	3903485	1975-09-01	Dolby, Ray Milton			
	3	4024346	1977-05-01	McDermott, Noel C.			
	4	4705967	1987-11-01	Vasile, Carmine F.			
	5	5220209	2069-09-12	Seymour, Michael J.			
	6	5723994	1998-03-01	Ting et al.			
	7	5936485	1999-08-01	Rylov, Sergey V.			
	8	5945878	1999-08-31	Westwick et al.			
	9	6259325	2001-07-01	Ishizuka et al.			
	10	6448847	2002-09-10	Paul et al.			
	11	6809581	2004-10-01	Rofougaran et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20050134378	2005-06-23	Westwick et al.			

	2	20050134386	2005-06-23	Westwick et al.			
	3	20050134375	2005-06-23	Westwick et al.			

Remarks

Note: Remarks are not for responding to an office action.

A paper IDS is being submitted on the same date as this electronic IDS.

Signature

Examiner Name	Date